

JOB POSTING

Senior IC and Systems Design Engineer

Manage the design and development of semiconductor ICs and PC boards for image sensors and cameras. Review and finalize documentation including technical drawings and specifications or electrical systems such as circuit schematic diagrams and layout drawings. Manage and oversee the testing and manufacturing of ICs and PC boards. Assess the analog, mixed-signal and digital system design, simulation, and layout for visible and infrared cameras. Develop manufacturing, test, and qualification procedures for the production of PC boards for cameras and optoelectronic systems. Supervise the testing of samples of manufactured end-products to ensure product technical specifications are met. Manage PCB board fabrication and assembly for test system and camera development. Inspect finished products to ensure they meet intended product specifications, quality standards, and safety compliance standards. Develop systems to ensure that all finished products are tested and that the results are documented in compliance with quality management system (QMS) guidelines before being distributed. Supervise support engineers and IT staff to perform CAD tool installation and the setup of servers for CAD design and simulations. Manage development efforts for ICs and system development in coordination with clients and stakeholders. Respond as a senior member of the IP incident report team to address complex individual and multi-party matters of concern. Stay abreast of the latest technological advancements and industry trends in order to optimize system products used in digital imaging.

Requirements: The position requires a Master's degree in Electrical Engineering, Electronics Engineering, Electrical Engineering Technology or a related field and two years of experience in the job offered or a related position. This requirement may be substituted with a Bachelor's degree in Electrical Engineering, Electronics Engineering, Electrical Engineering Technology or a related field and five years of experience in the job offered or a related position. The position requires skills and knowledge in Schematic design and Layout design using Mentor PADS tool, Cadence ASIC tools, FPGA RTL simulation using Xilinx Vivado, Image capture and analysis using Python and Matlab, IC probe test using analytical probe station, and image sensor data analysis and report generation.

Salary: \$147,000.00 per year

40 hours/week

Job Site: Camarillo, CA.

Please send resumes to Atul Joshi, President and CEO, Saaz Micro, Inc. at 2545 W. Hillcrest Dr., Suite 200, Thousand Oaks, CA 91320 or via email to info@saaztechnology.com.

This job notice is being posted in connection with the filing of a Permanent Alien Employment Certification Application. Any person may provide documentary evidence bearing on the application to the Certifying Officer, Office of Foreign Labor Certification, 200 Constitution Avenue, NW, Room N-5311, Washington, DC 20210.